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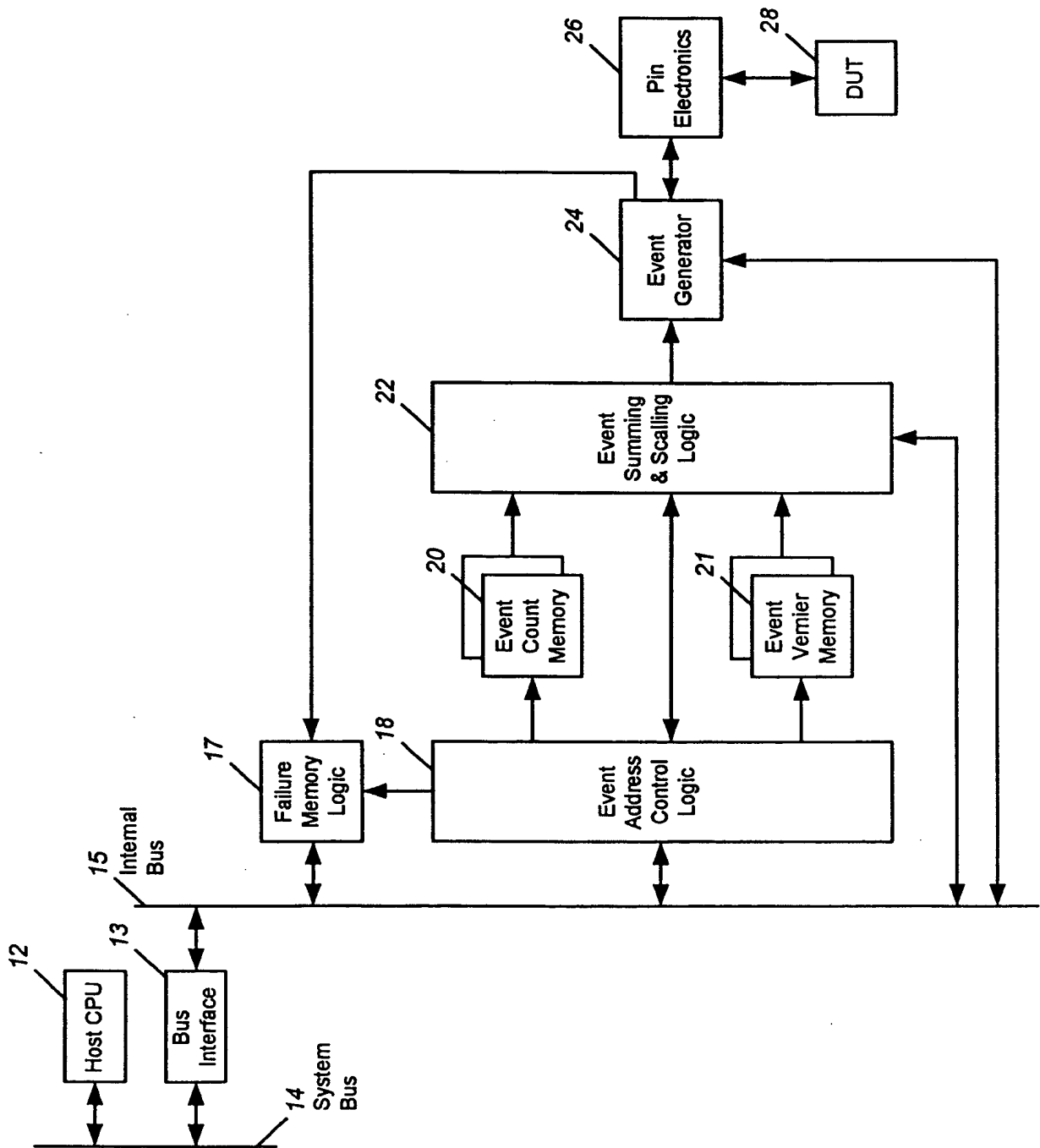


FIG. 1

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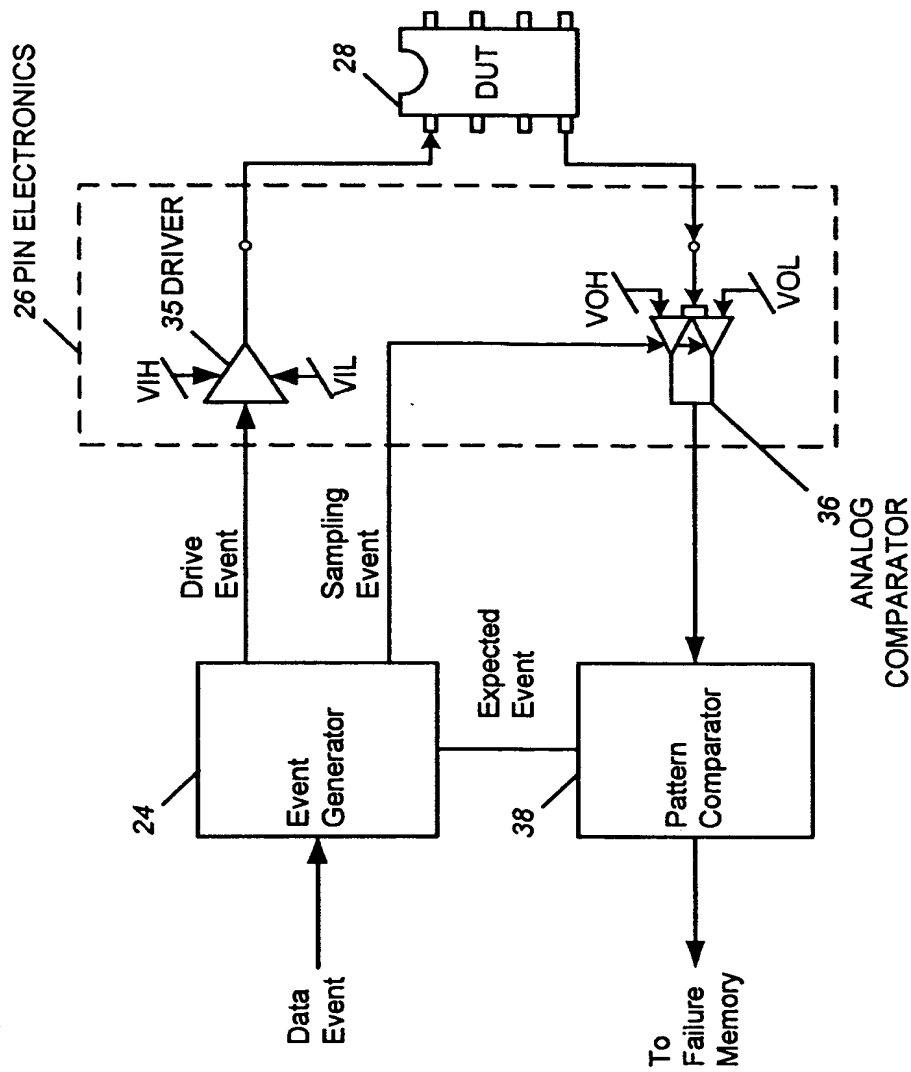


FIG. 2

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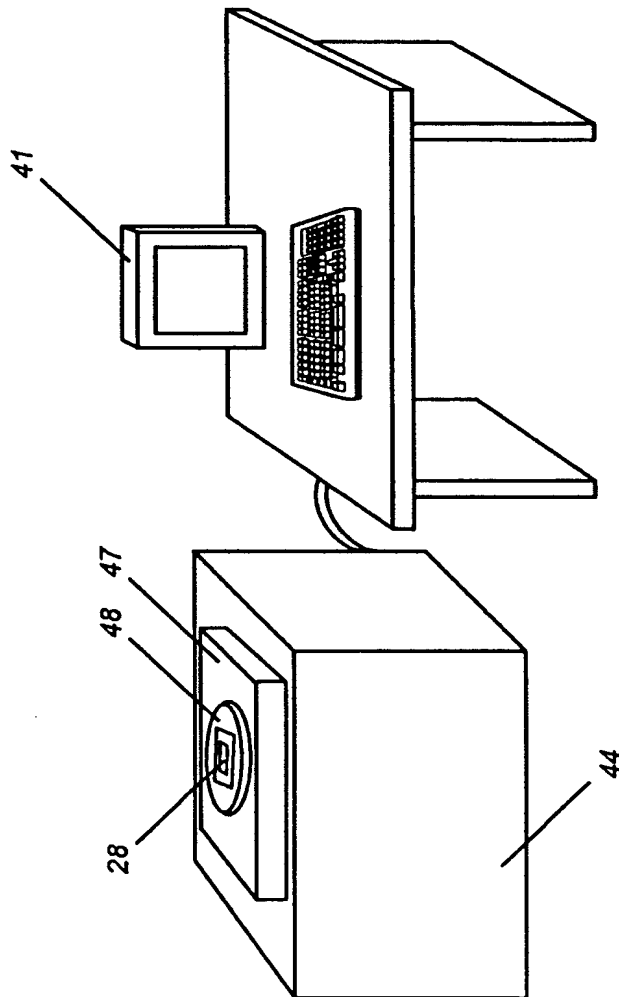


FIG. 3

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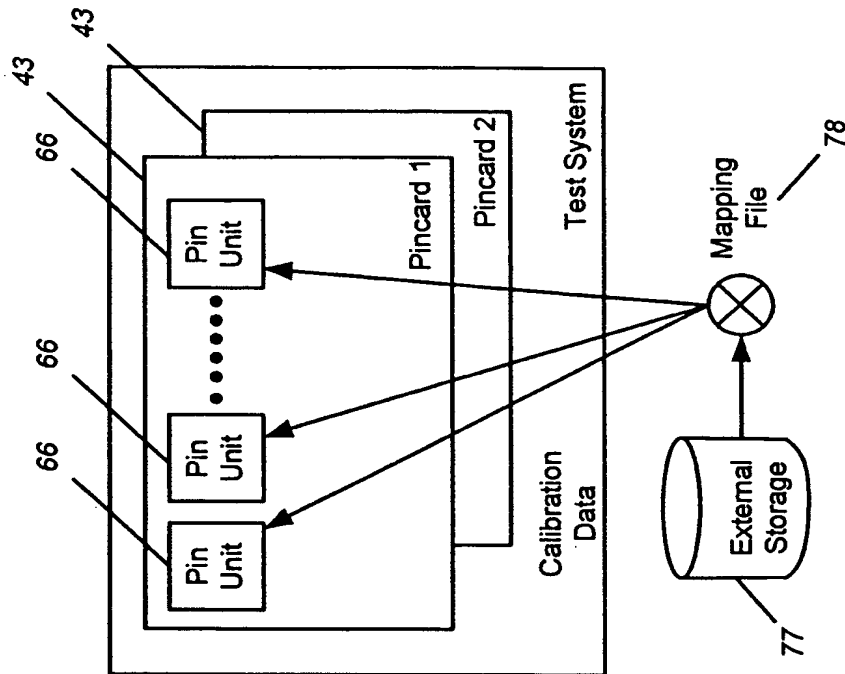


FIG. 5

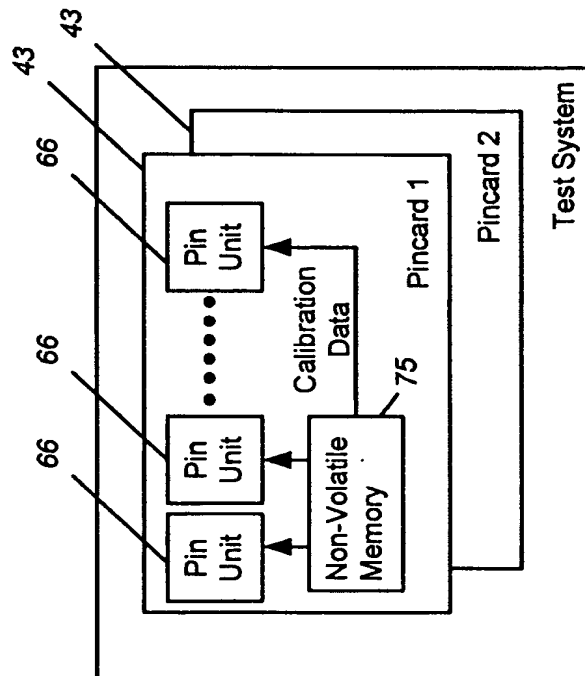


FIG. 4

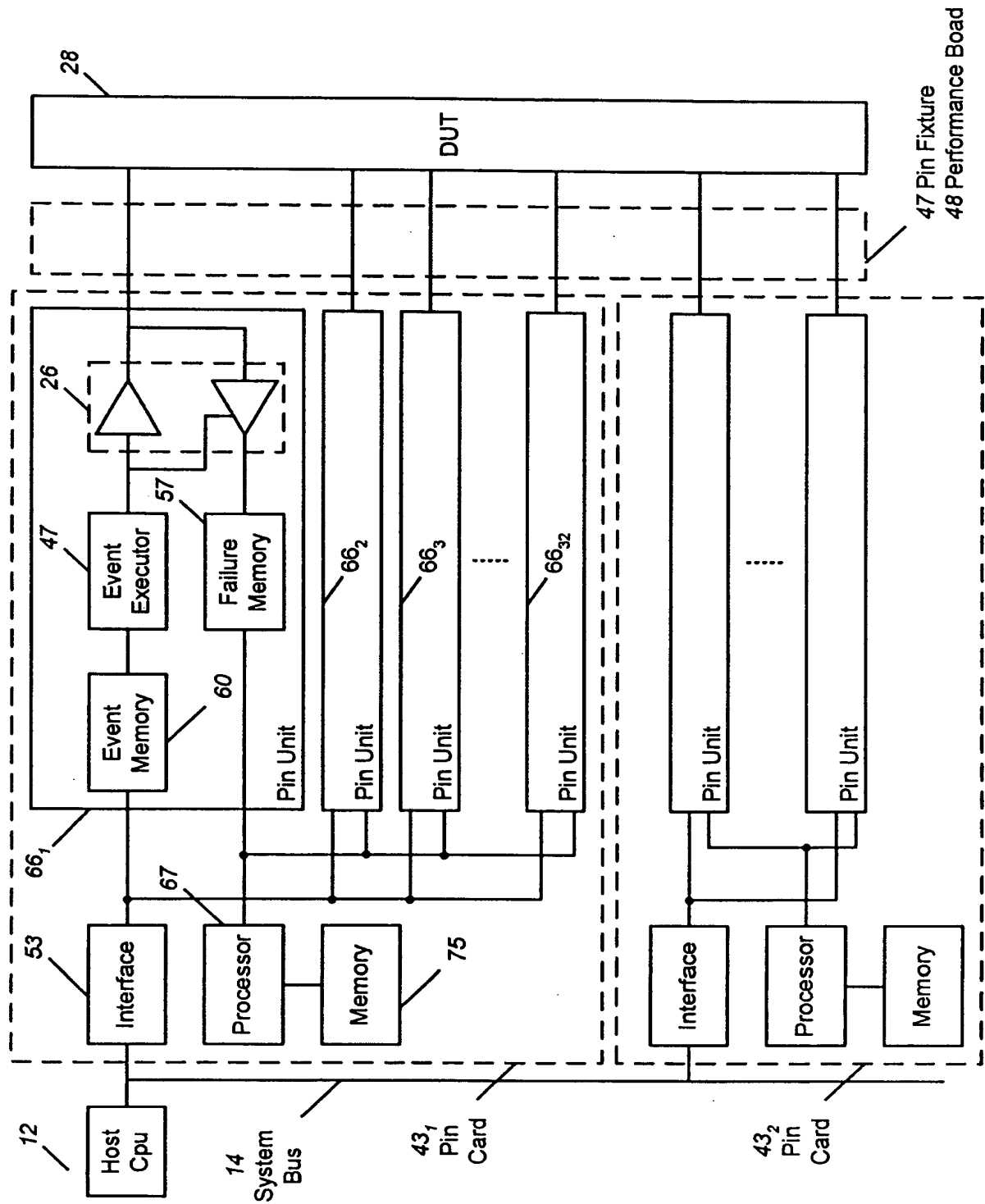


FIG. 6

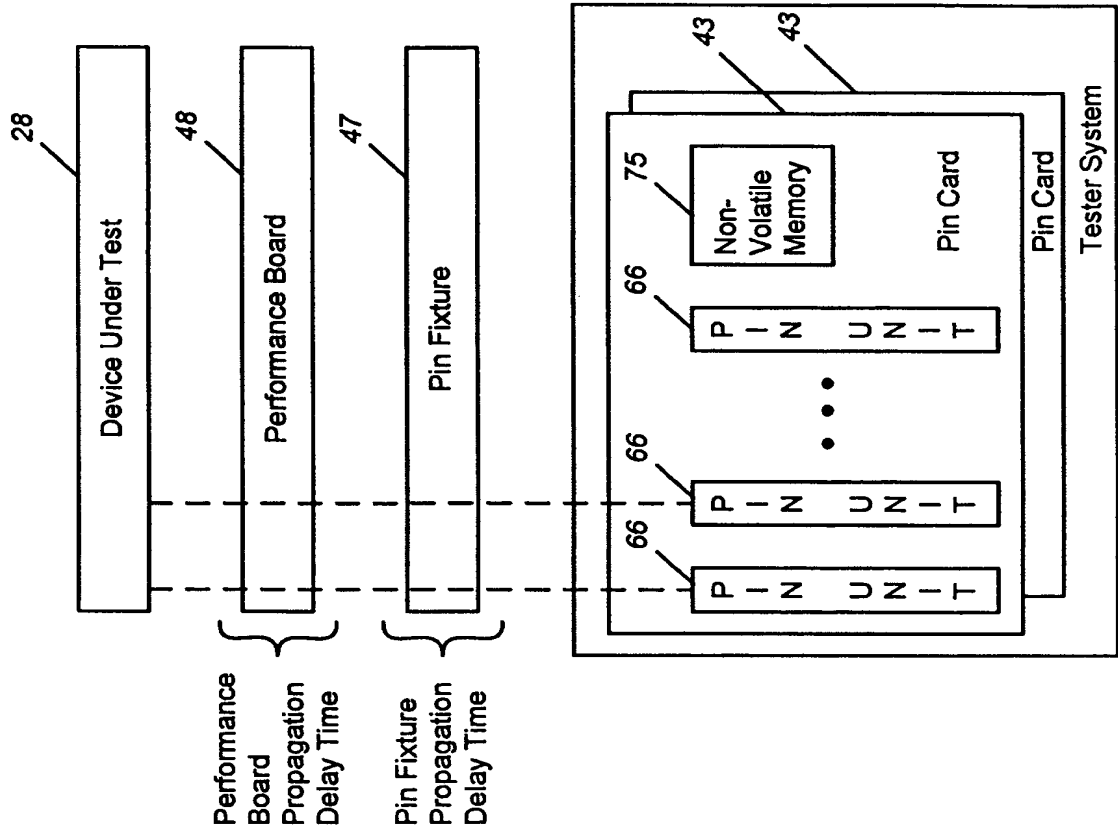


FIG. 7

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```
Calibration_Data_Configure()
{
    struct PIN_UNIT {
        unsigned int corcv; /* correction offset to reference comparison voltage register */
        unsigned int cordv; /* correction offset to reference driving voltage register */
    } *pin_unit;
    struct CALIBRATION_MEMORY {
        /* calibration data for correction offset to reference comparison voltage */
        unsigned int corcv;

        /* calibration data for correction offset to reference comparison voltage */
        unsigned int corcv;

        /* Base address of calibration memory data */
        #define CALIBRATION_MEMORY_BASE
        ((struct CALIBRATION_MEMORY *) 0x100)

        /* Base address of pin units calibration registers */
        #define PIN_UNIT_BASE ((struct PIN_UNIT *) 0x200)
        int unit_count;
        pin_unit = PIN_UNIT_BASE;
        calibration_memory = CALIBRATION_MEMORY_BASE;
        for(unit_count=0 ; unit_count <8; unit_count++)
        {
            pin_unit[unit_count].corcv = calibration_memory[unit_count].corcv;
            pin_unit[unit_count].cordv = calibration_memory[unit_count].cordv;
        }
    }
}
```

FIG. 8

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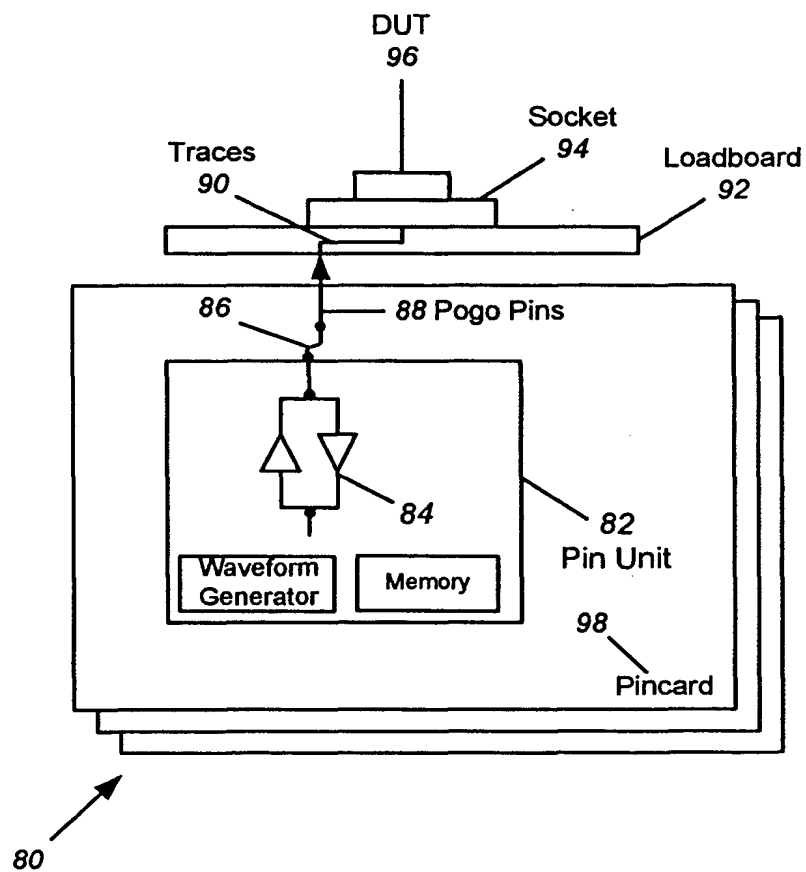


FIG. 9

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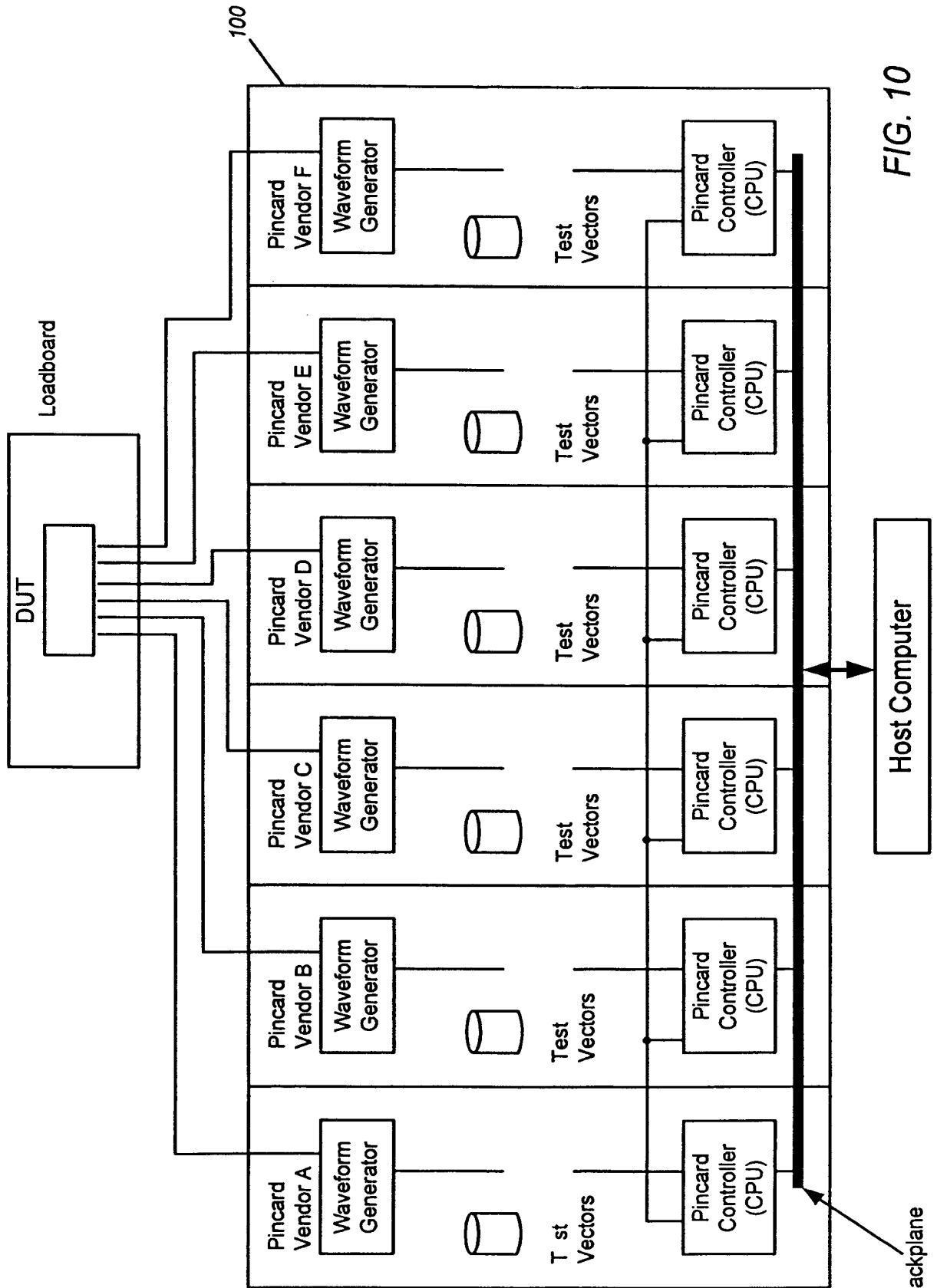


FIG. 10

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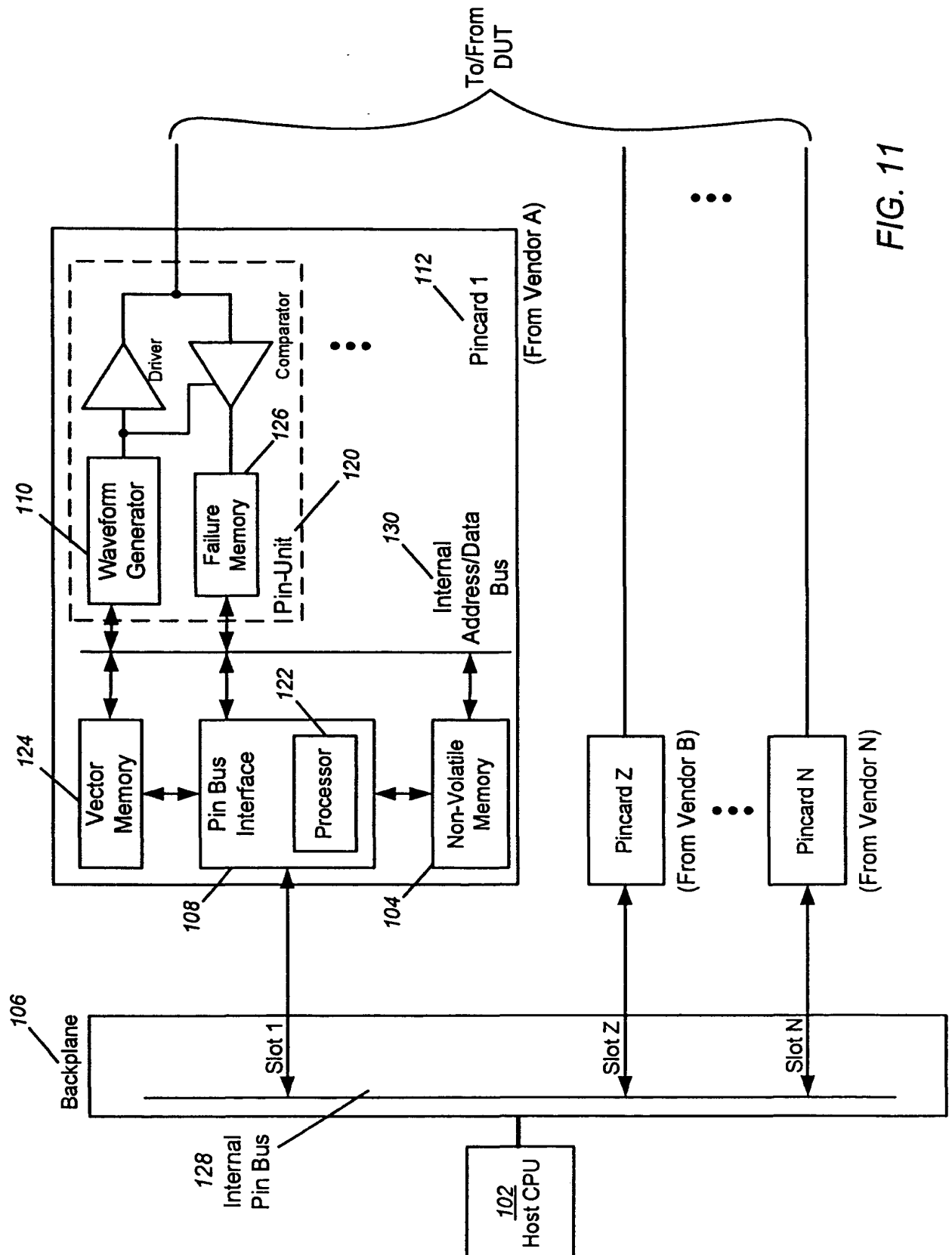


FIG. 11

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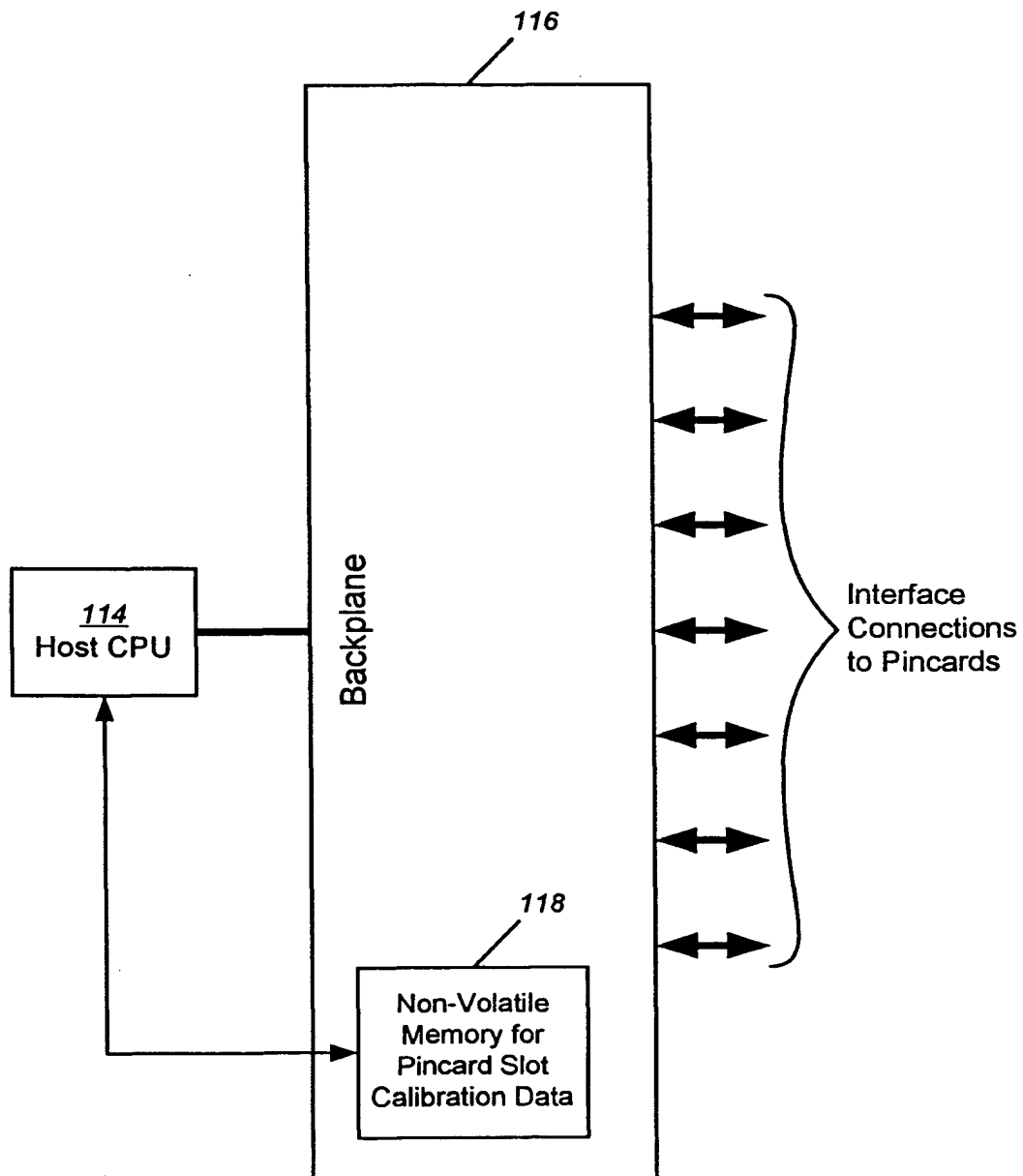


FIG. 12

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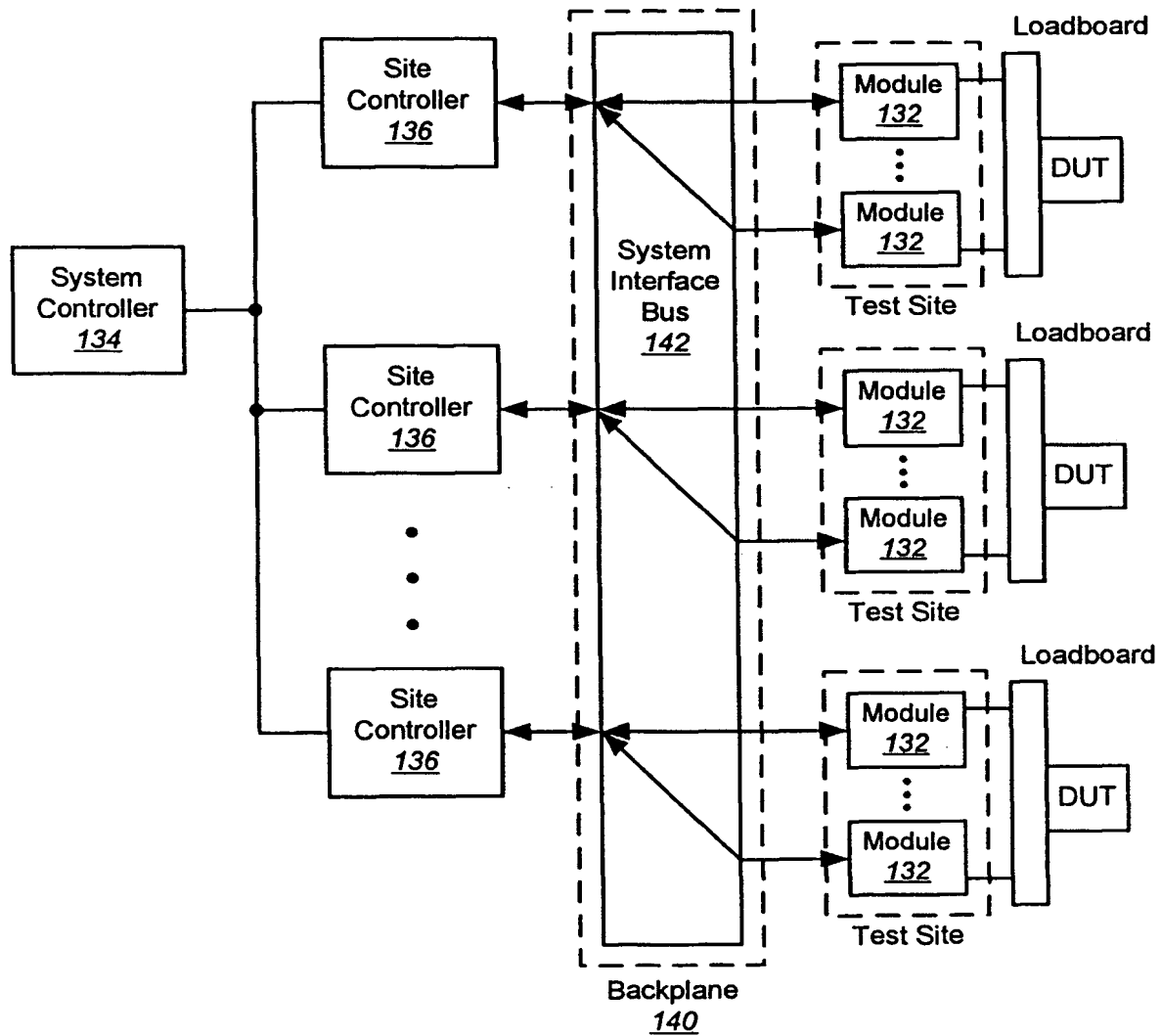


FIG. 13

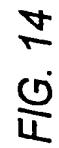


FIG. 14